

## WEST Search History

DATE: Monday, April 18, 2005

| Hide?                    | <u>Set</u><br><u>Name</u>                                     | <u>Query</u>  | <u>Hit</u><br><u>Count</u> |
|--------------------------|---|---|----------------------------|
|                          | <i>DB=USPT; PLUR=YES; OP=OR</i>                               |   |                            |
| <input type="checkbox"/> | L13   | L12 not L6  | 1871                       |
| <input type="checkbox"/> | L12   | L11 and (pinned or MRAM or spacer or (alternat\$4 with oxide) or peripher\$4 or circumfer\$7) | 1908                       |
| <input type="checkbox"/> | L11   | L9 or L10   | 5207                       |
| <input type="checkbox"/> | L10   | 365/48,50,51,55,56,66,69,129,145,158,171-173.ccls.  | 4192                       |
| <input type="checkbox"/> | L9  | 438/3.ccls.   | 1078                       |
| <input type="checkbox"/> | L8  | 6737283.pn.   | 1                          |
|                          | <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i> |   |                            |
| <input type="checkbox"/> | L7  | 6737283.pn.   | 2                          |
| <input type="checkbox"/> | L6  | L5 and (MRAM or PCRAM or nitride or carbide or oxide or dioxide or thickness)                 | 1554                       |
| <input type="checkbox"/> | L5  | L4 and L3   | 1842                       |
| <input type="checkbox"/> | L4  | spacer with between   | 201378                     |
| <input type="checkbox"/> | L3  | ((memory or cell) with stack) and spacer  | 3926                       |
| <input type="checkbox"/> | L2  | MRAM and pinned and tunnel and spacer   | 314                        |
|                          | <i>DB=USPT; PLUR=YES; OP=OR</i>                               |   |                            |
| <input type="checkbox"/> | L1  | (MRAM adj cell).ti.   | 14                         |

END OF SEARCH HISTORY